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ABSTRACT OF THE DISCLOSURE

A semiconductor processing apparatus for processing a semiconductor in a processing chamber separated from the air wherein the processing chamber contains a wafer stage on which there is positioned a wafer sensor module equipped with sensor probes, each sensor probe capable of detecting at least one of electric current, voltage and temperature of an article to be processed and placed on the wafer sensor module, which is carried into the processing chamber by a transporting means for the article to be processed, and detected values by the sensor probes being converted to optical signals and led to outside of the processing chamber, can optimize conditions for processing the article easily and in a short time without lowering throughput.

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